

FIG. 1

Prior Art

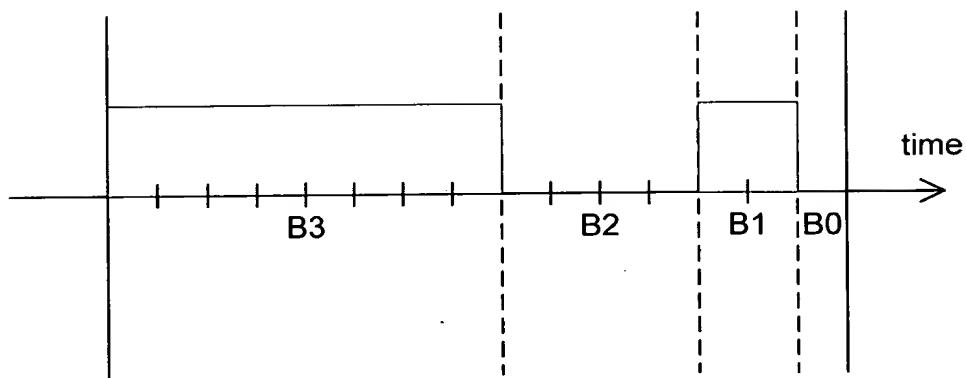


FIG. 2

Prior Art

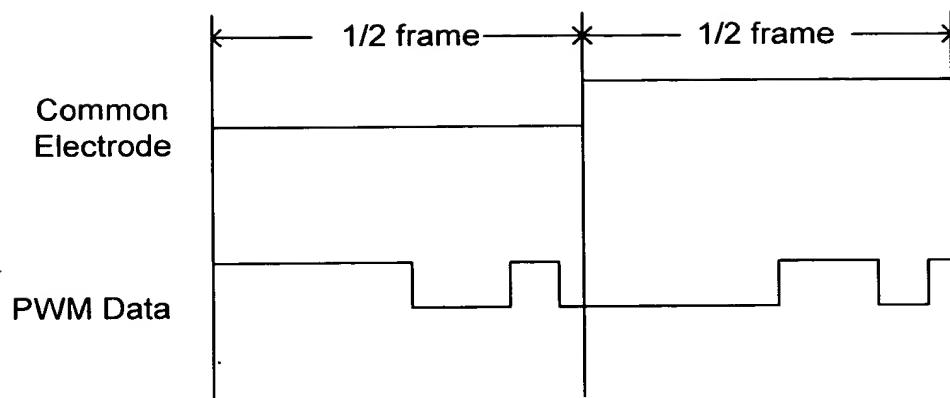


FIG. 3
Prior Art

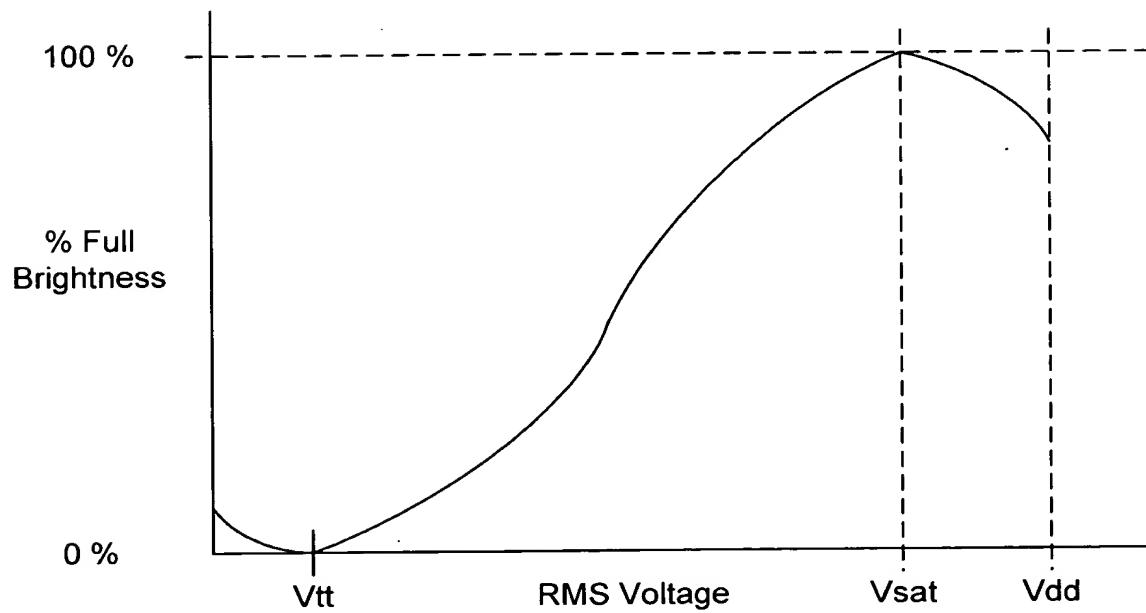


FIG. 4
Prior Art

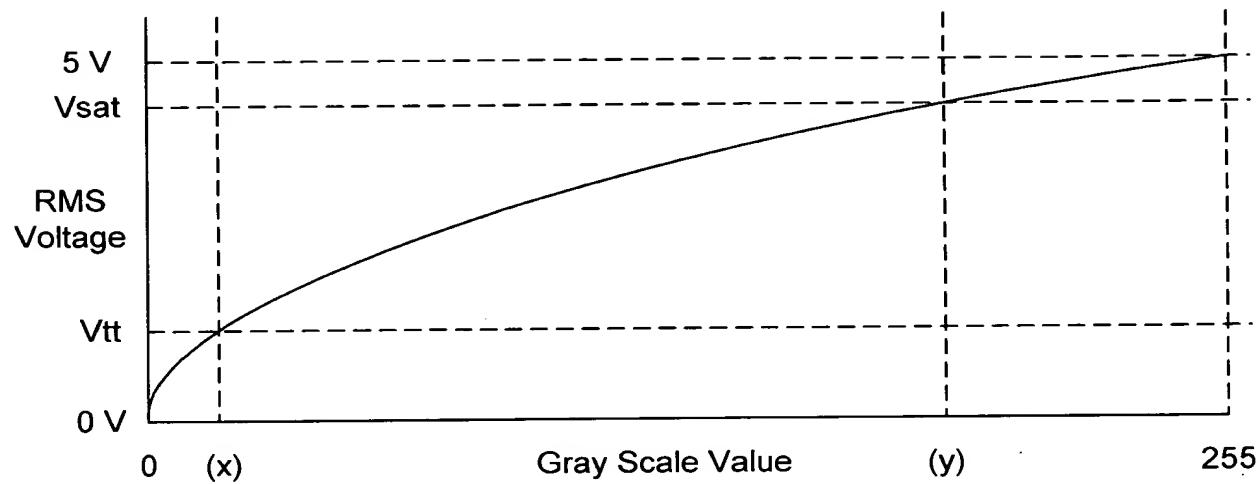


FIG. 5

Prior Art

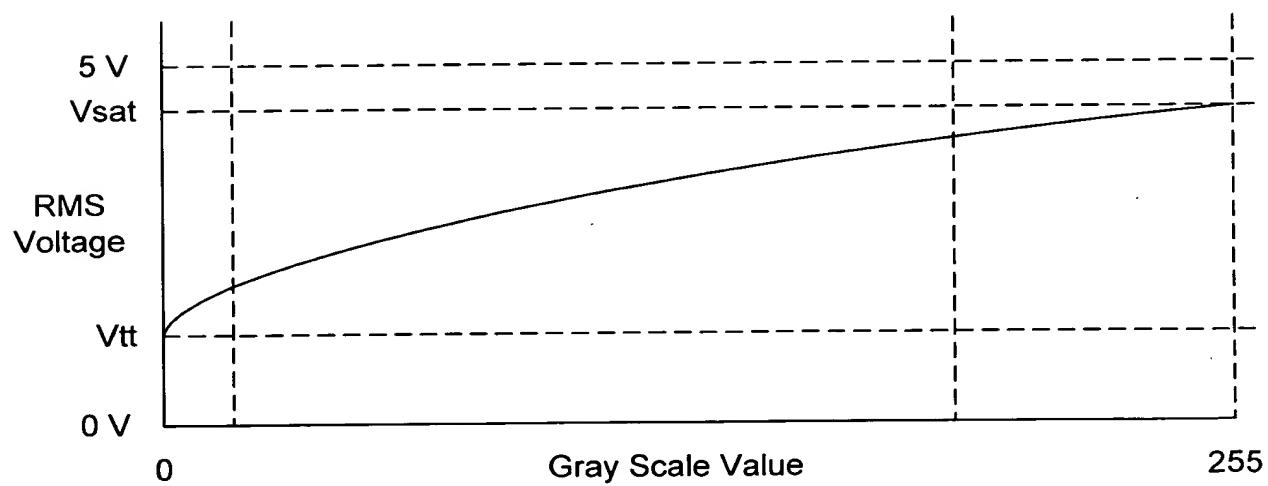


FIG. 11

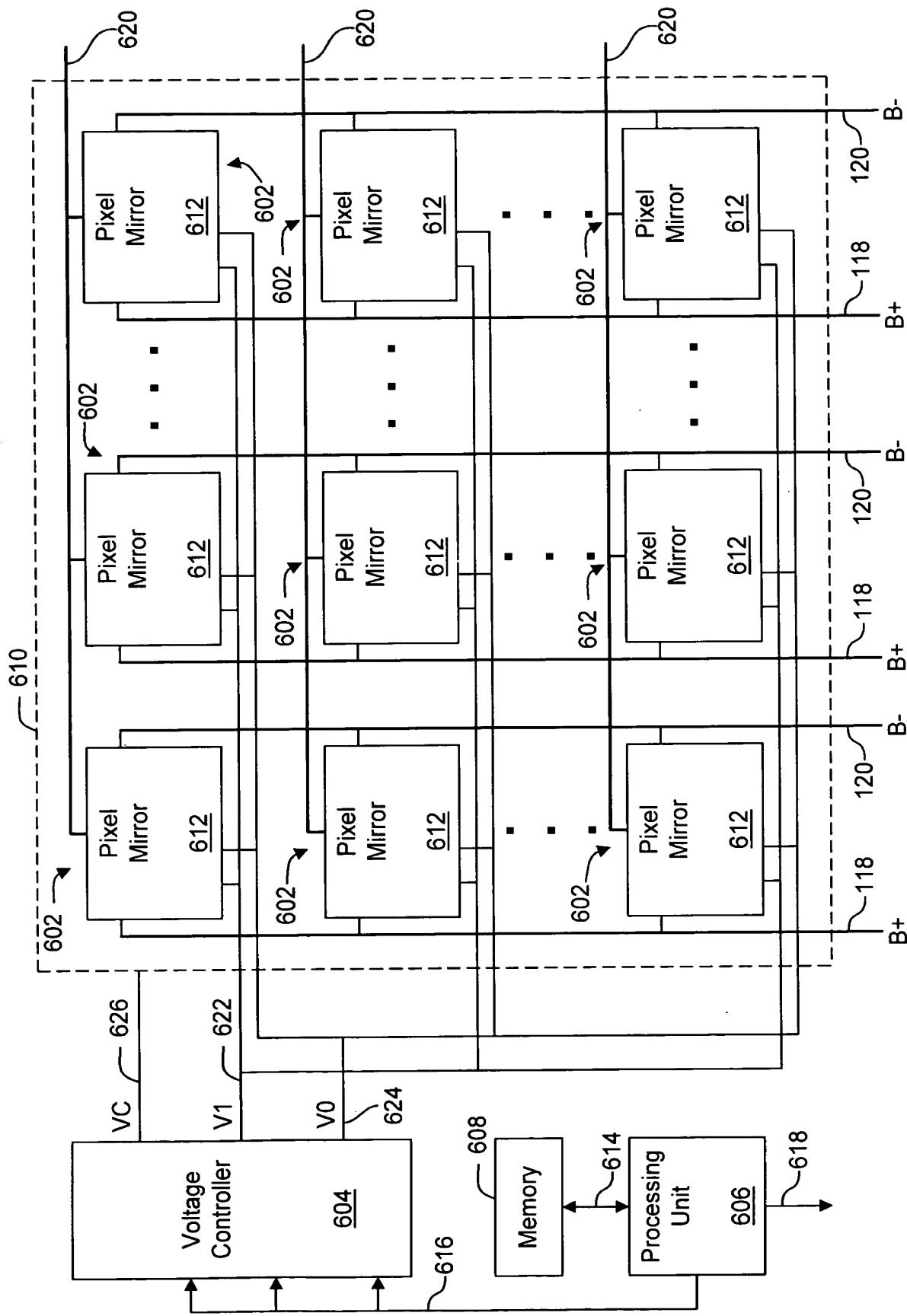


FIG. 6

600 →

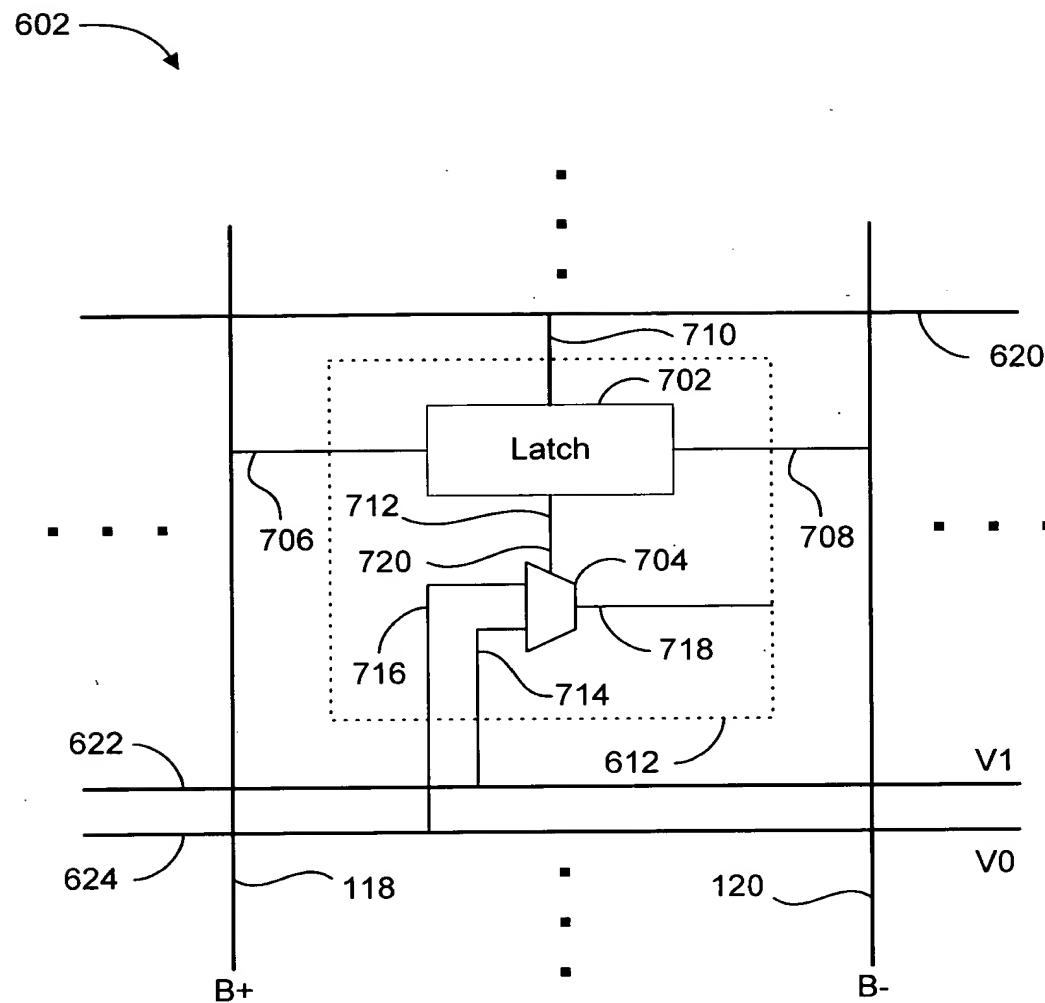


FIG. 7

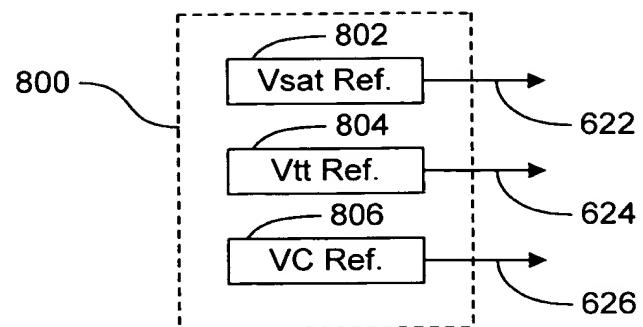


FIG. 8

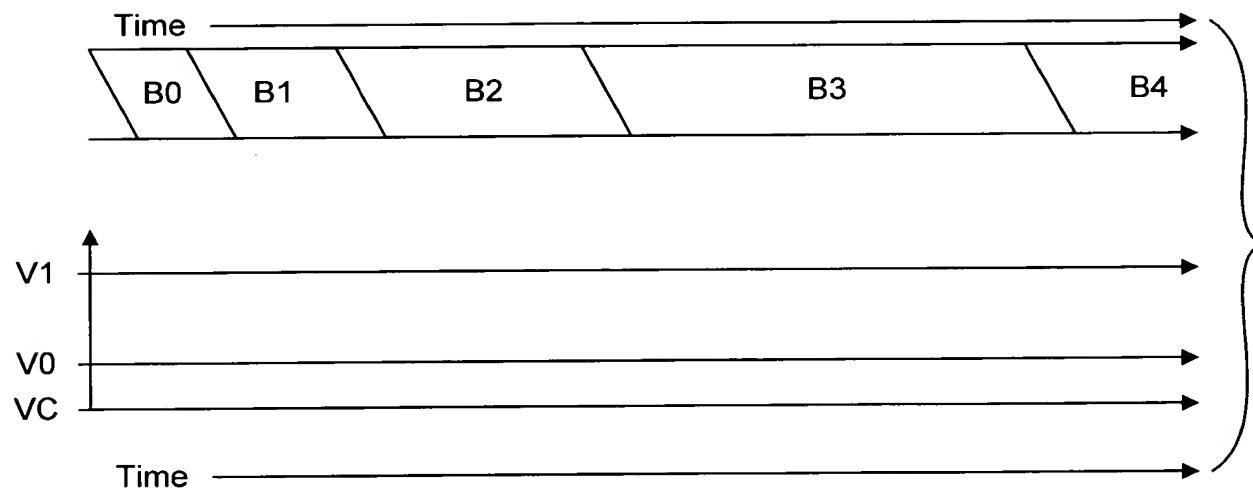


FIG. 9

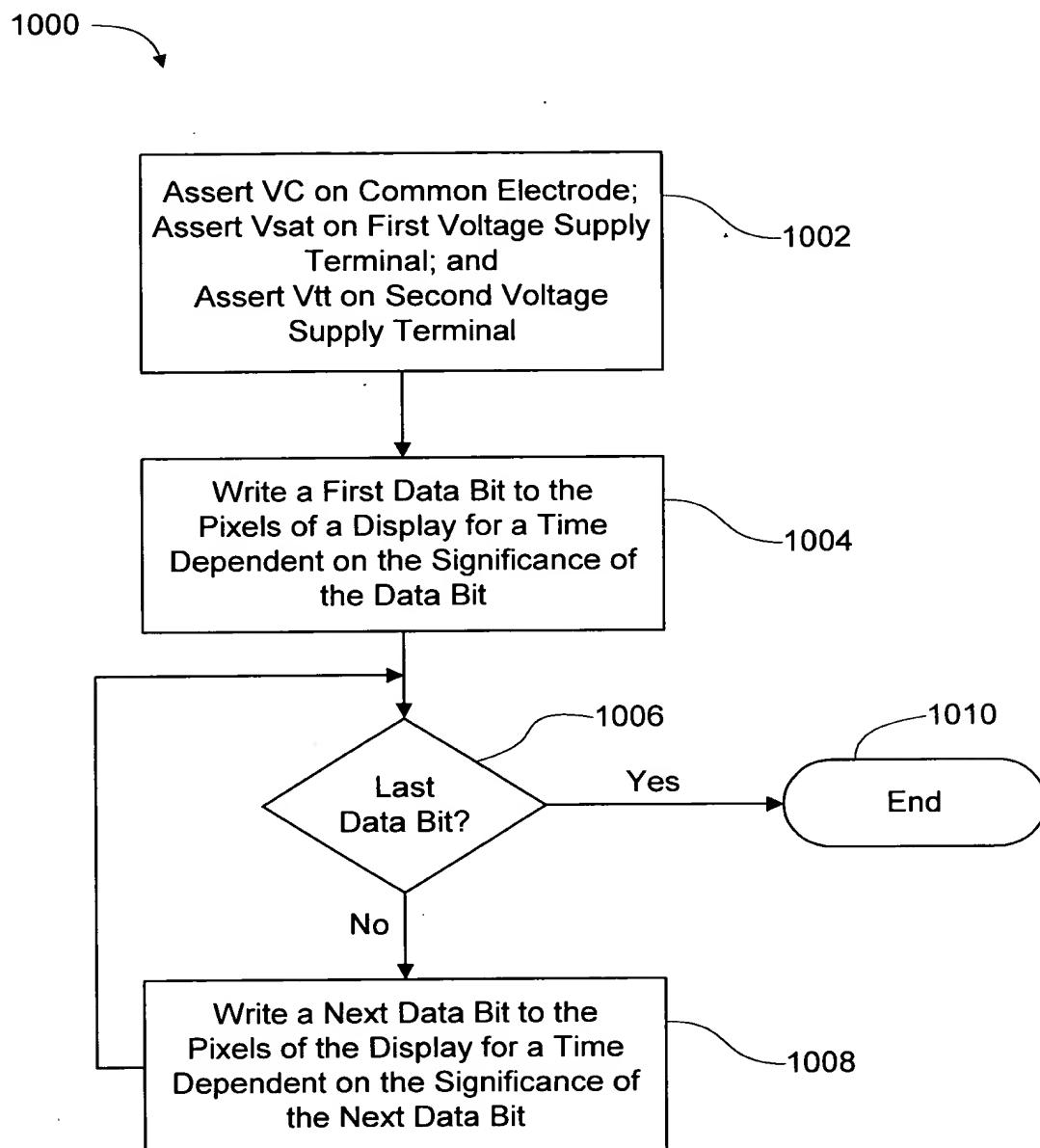


FIG. 10

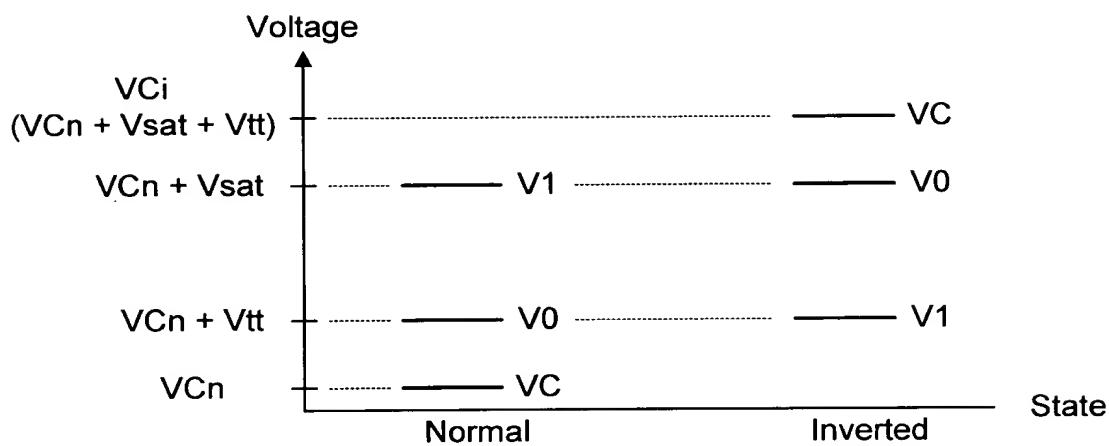


FIG. 12A

$VC_n = 0V$		
$V_{tt} = 1V$		
$V_{sat} = 3V$		
$VC_i = VC_n + V_{sat} + V_{tt} = 4V$		
	Normal	Inverted
VC	$0V$	$4V$
$V1$	$3V$	$1V$
$V0$	$1V$	$3V$

FIG. 12B

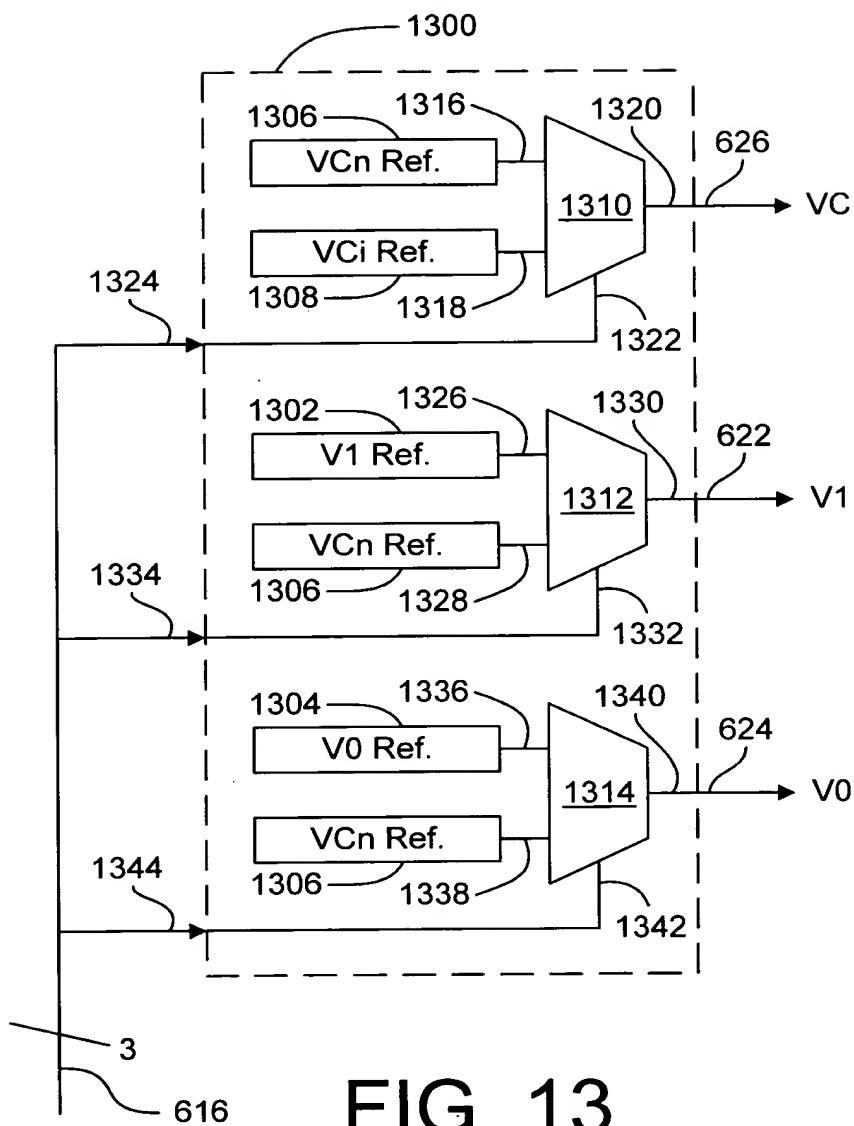


FIG. 13

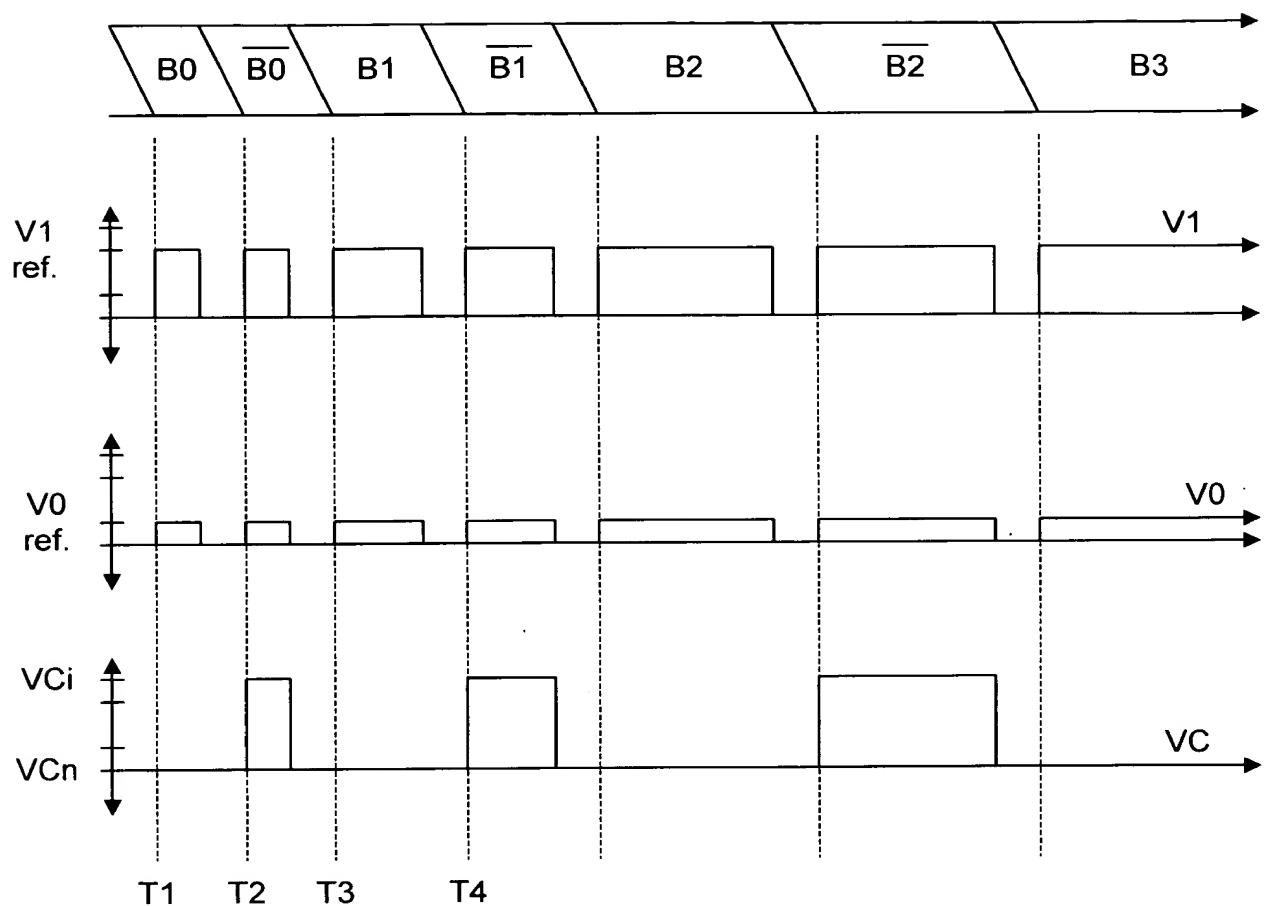


FIG. 14

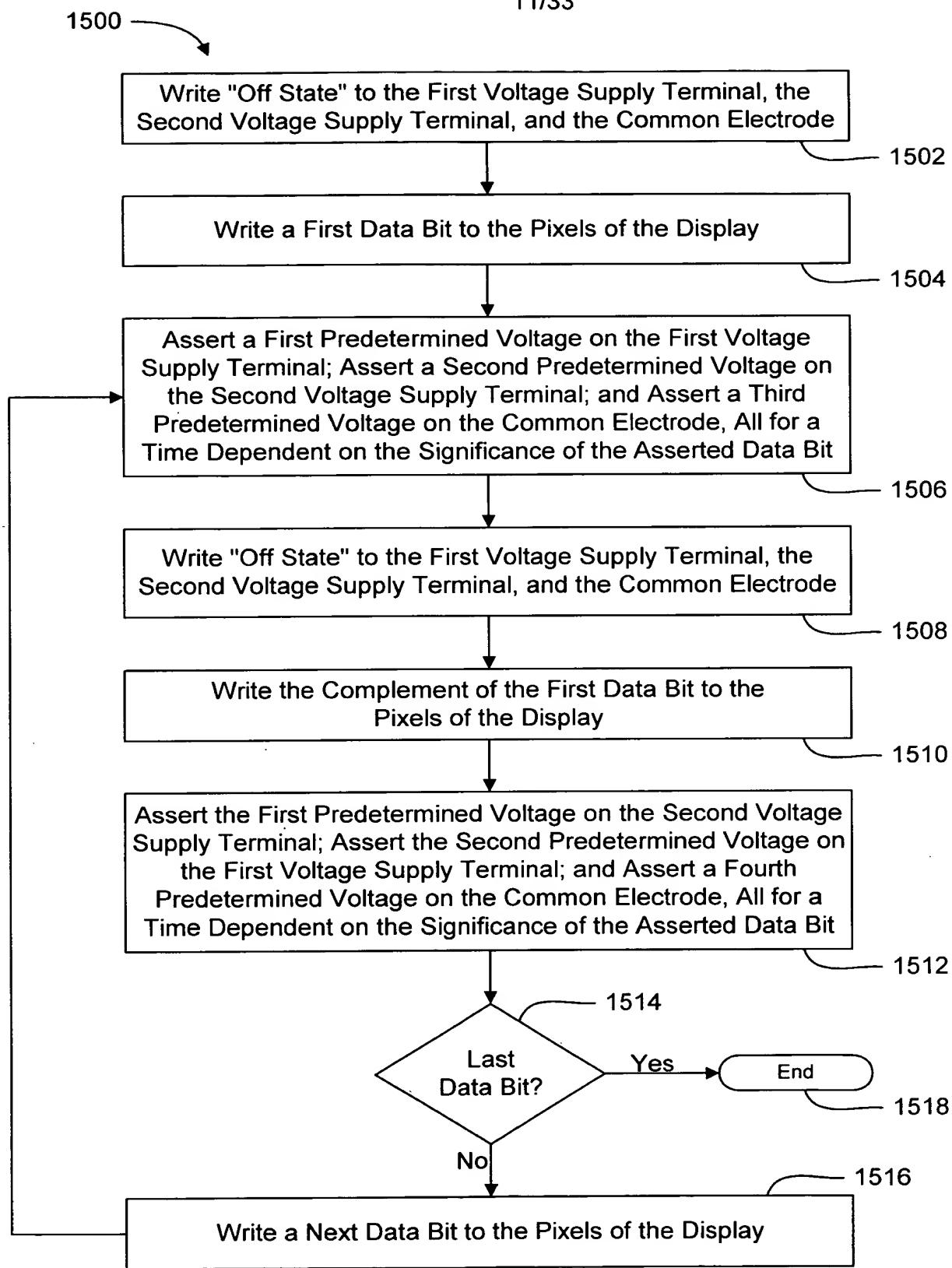


FIG. 15

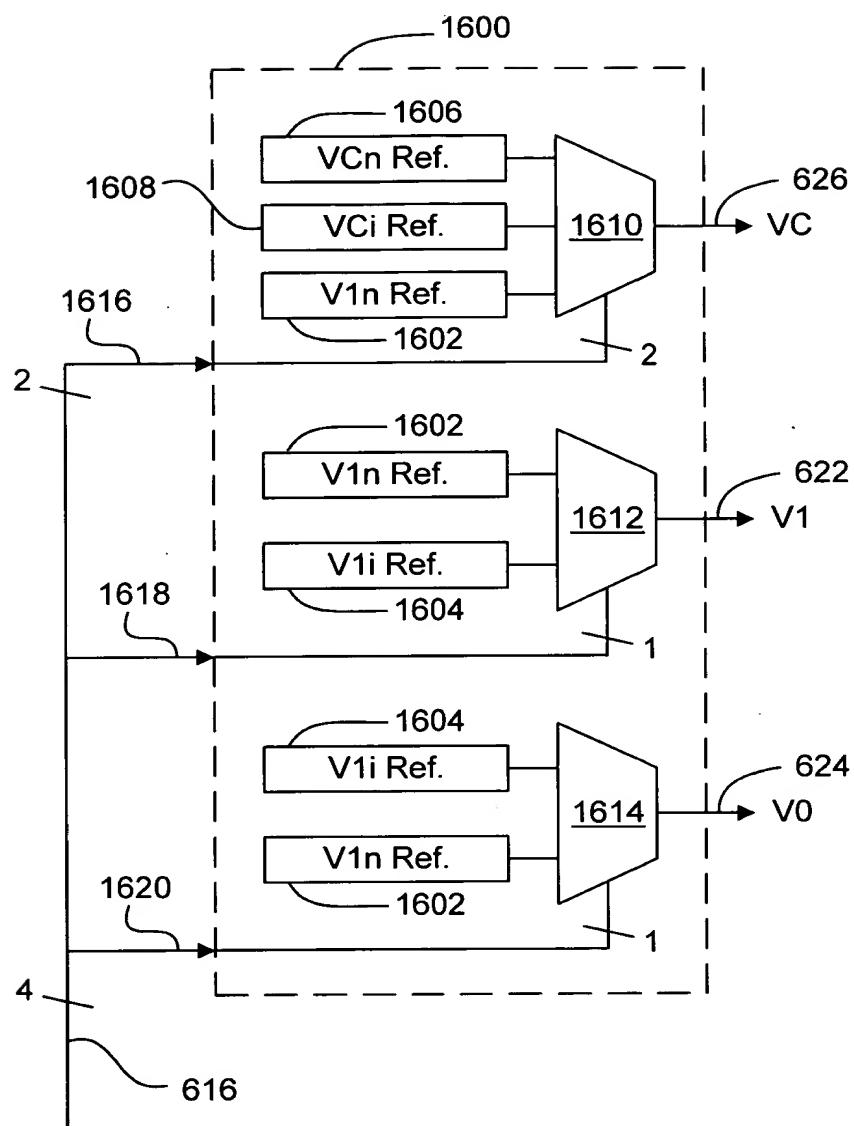


FIG. 16

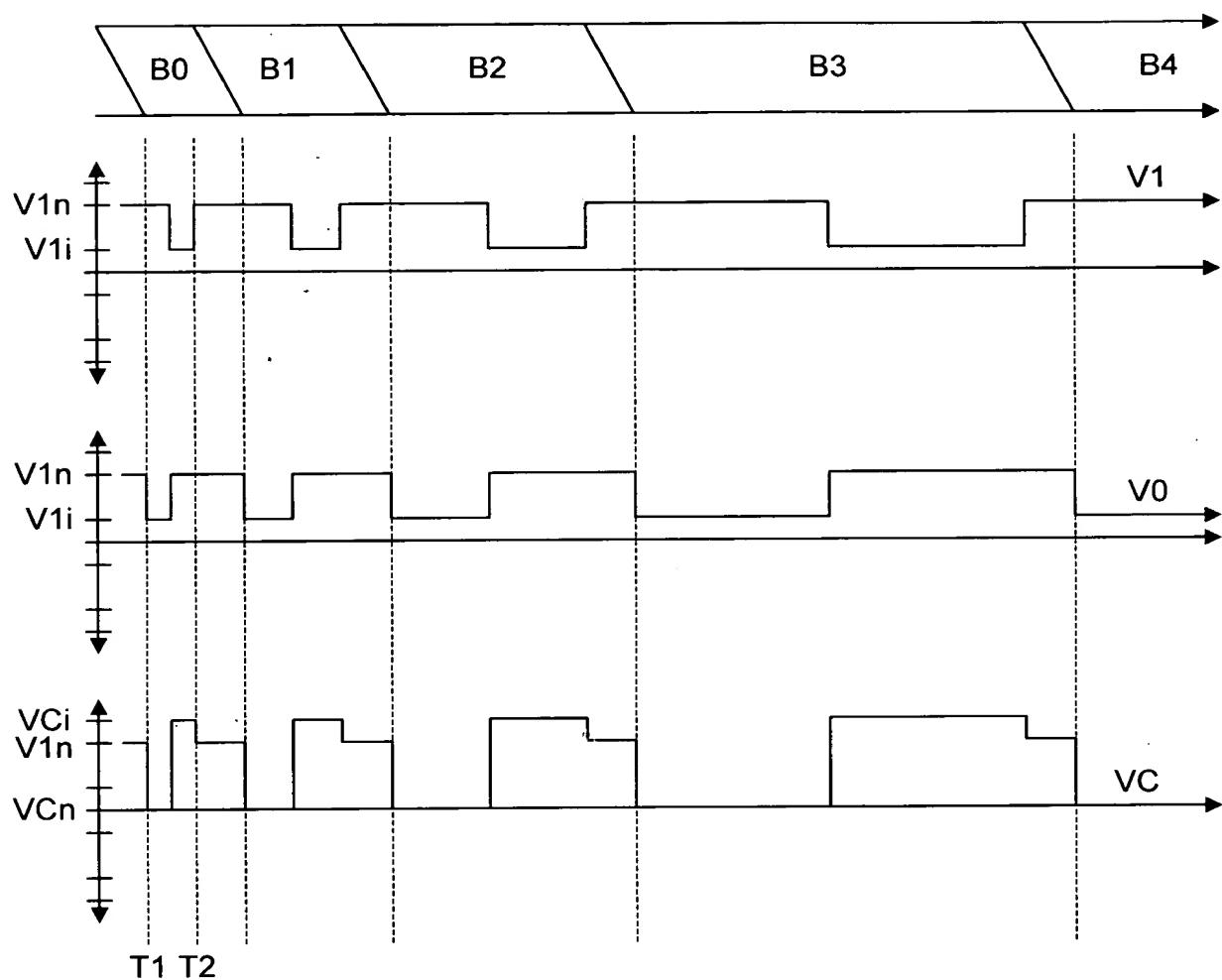


FIG. 17

1800

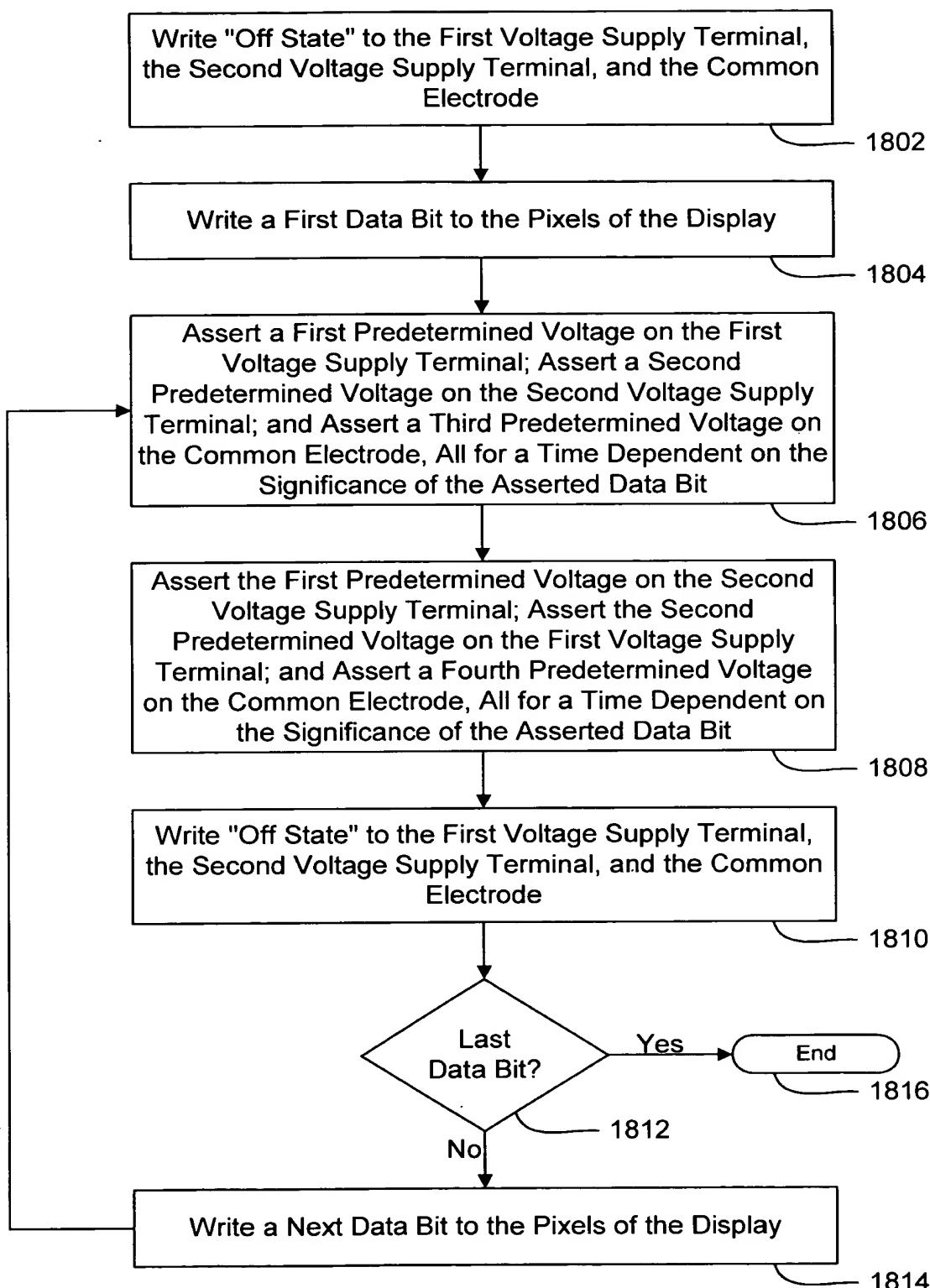


FIG. 18

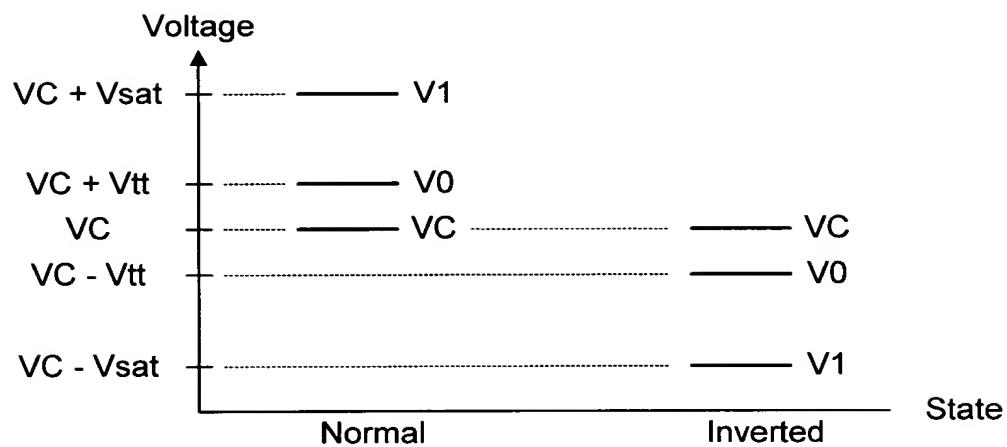


FIG. 19A

$VC = 3V$		
$V_{tt} = 1V$		
$V_{sat} = 3V$		
	Normal	Inverted
VC	3V	3V
$V0$	4V	2V
$V1$	6V	0V

FIG. 19B

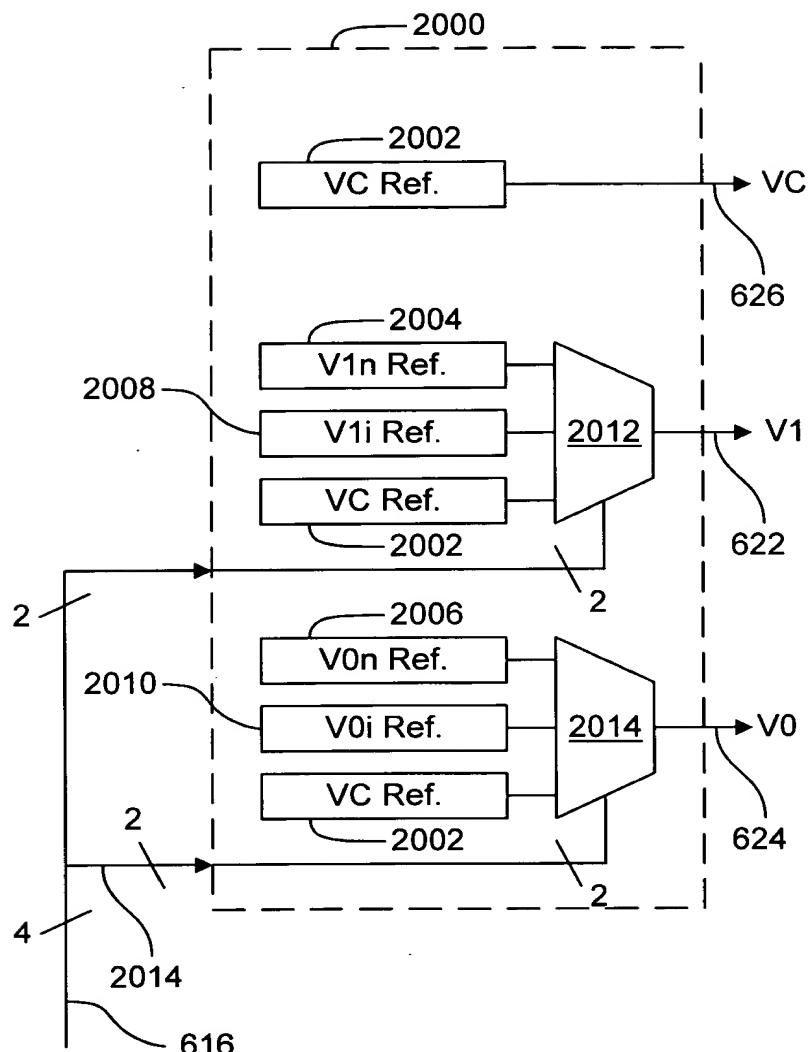


FIG. 20

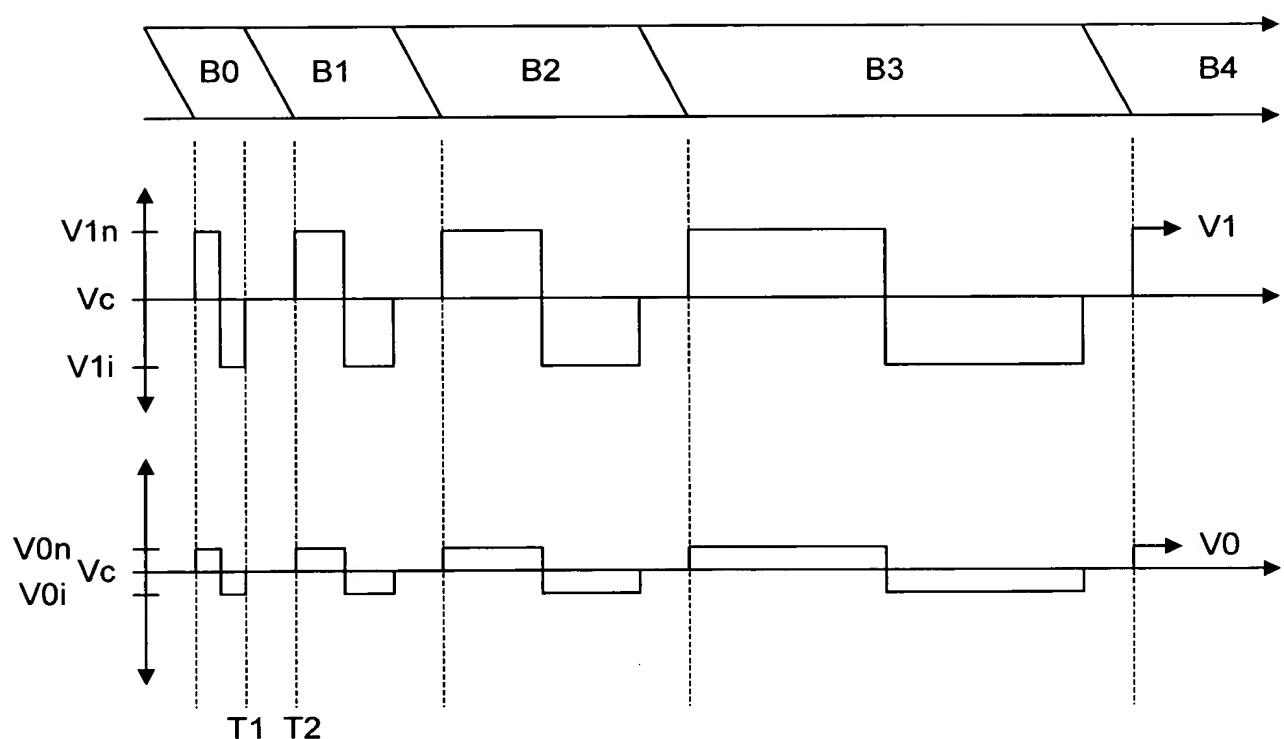


FIG. 21A

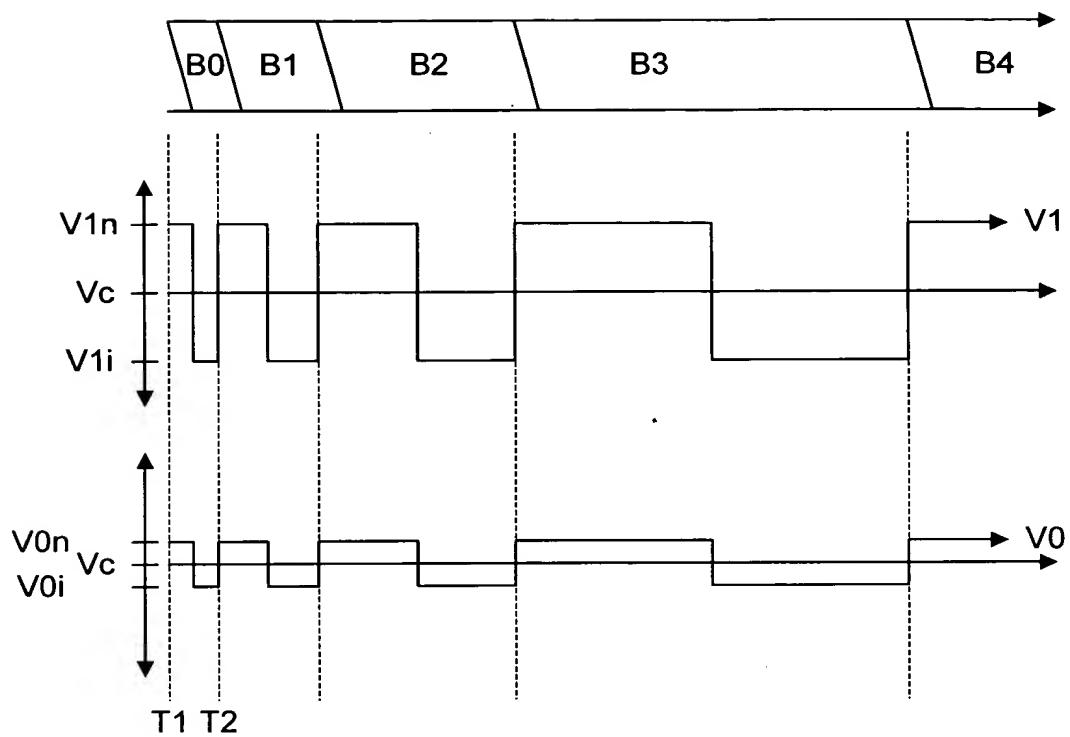


FIG. 21B

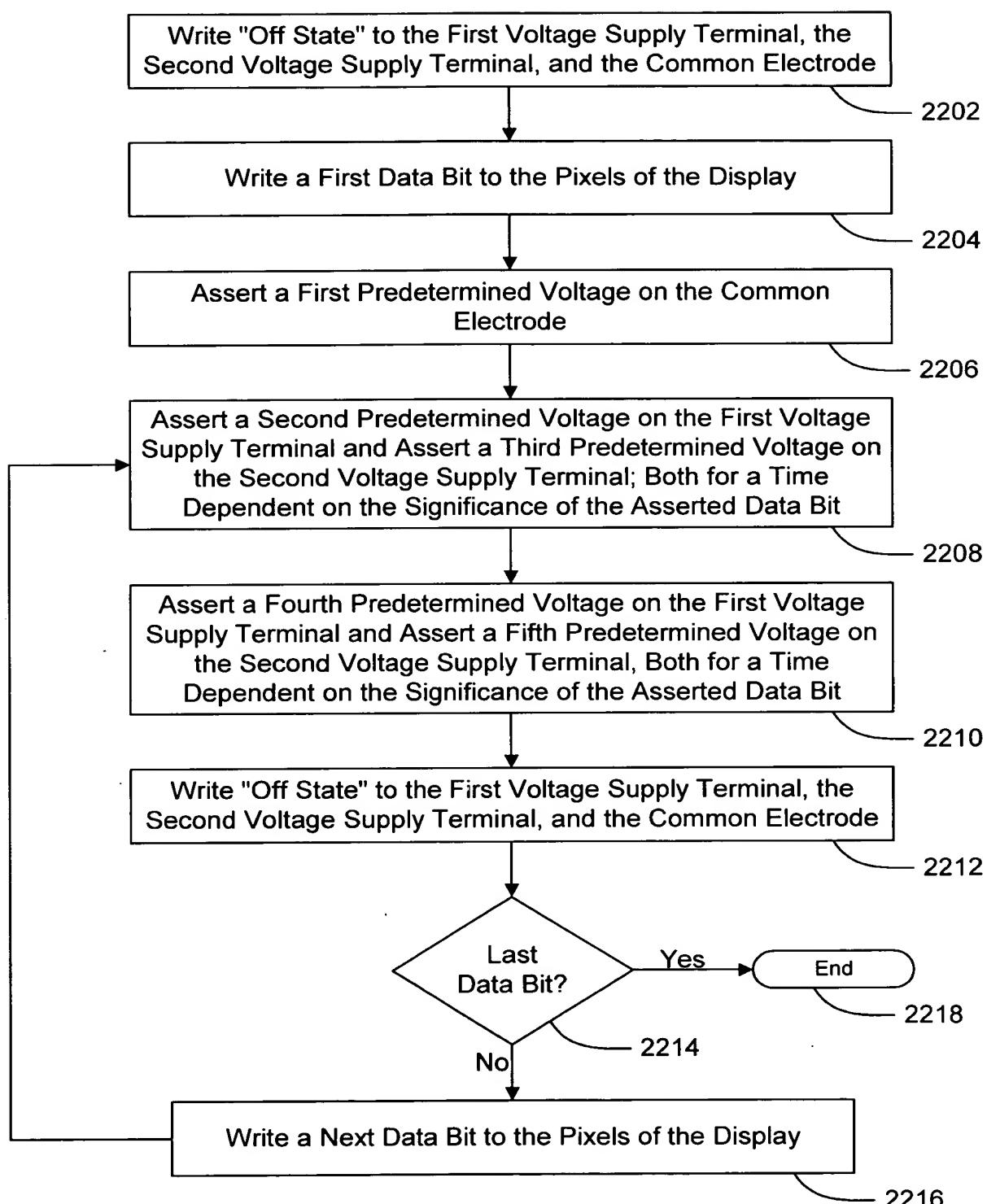


FIG. 22

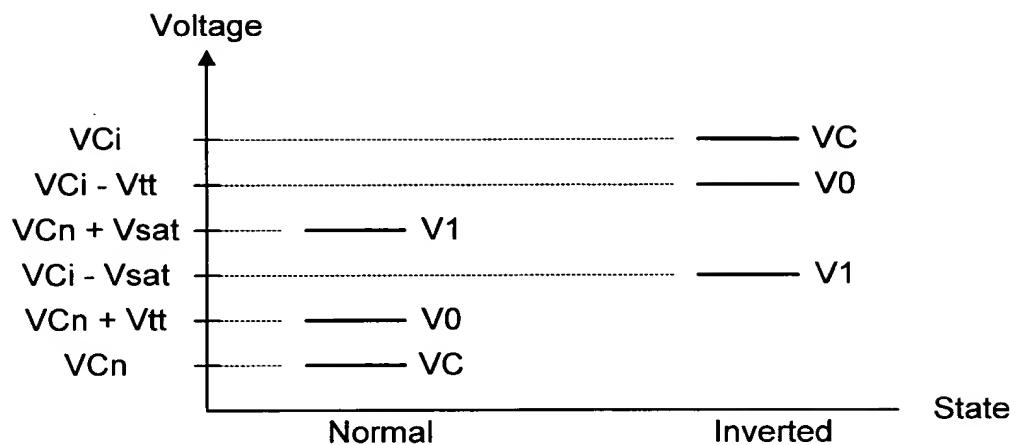


FIG. 23A

$VC_n = 0V$		
$VC_i = 5V$		
$V_{tt} = 1V$		
$V_{sat} = 3V$		
	Normal	Inverted
VC	0V	5V
V_0	1V	4V
V_1	3V	2V

FIG. 23B

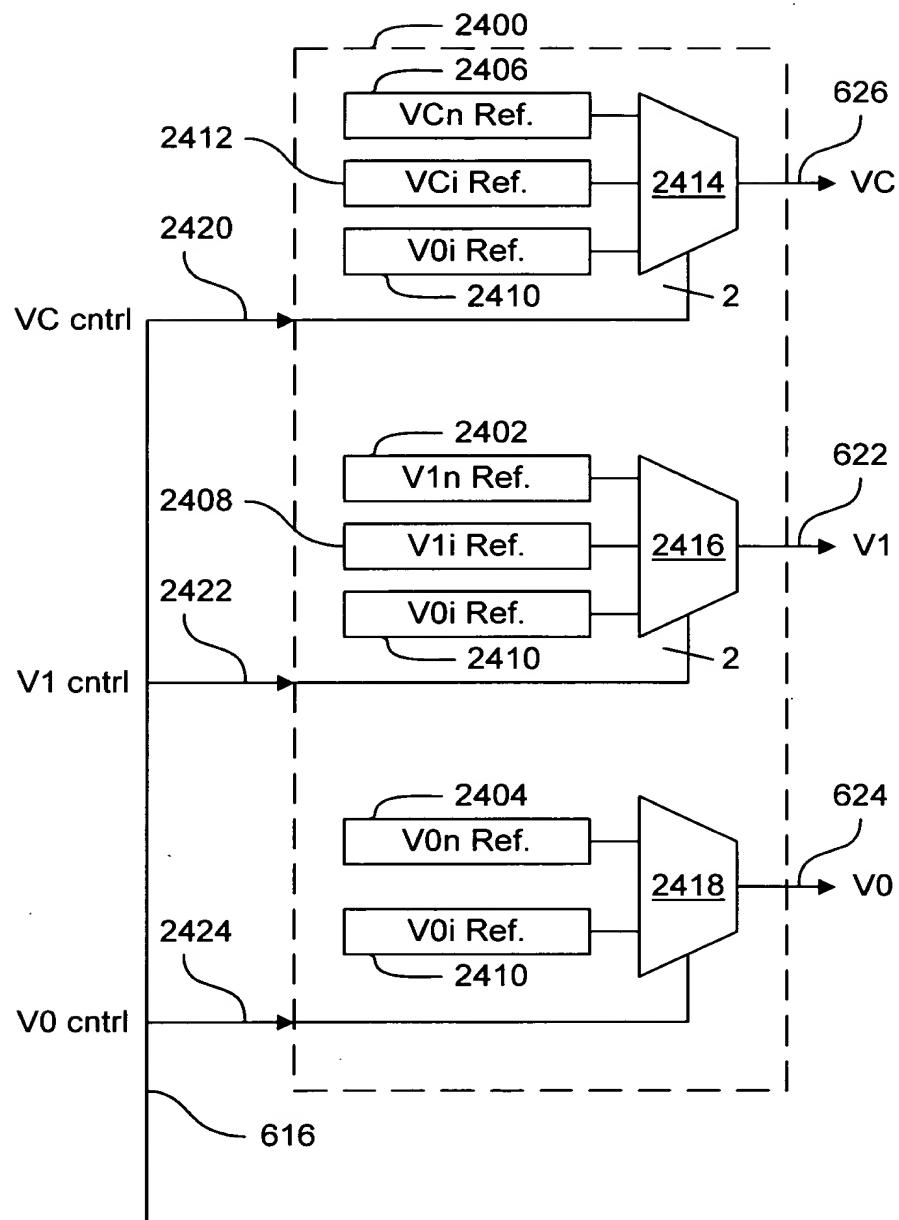


FIG. 24

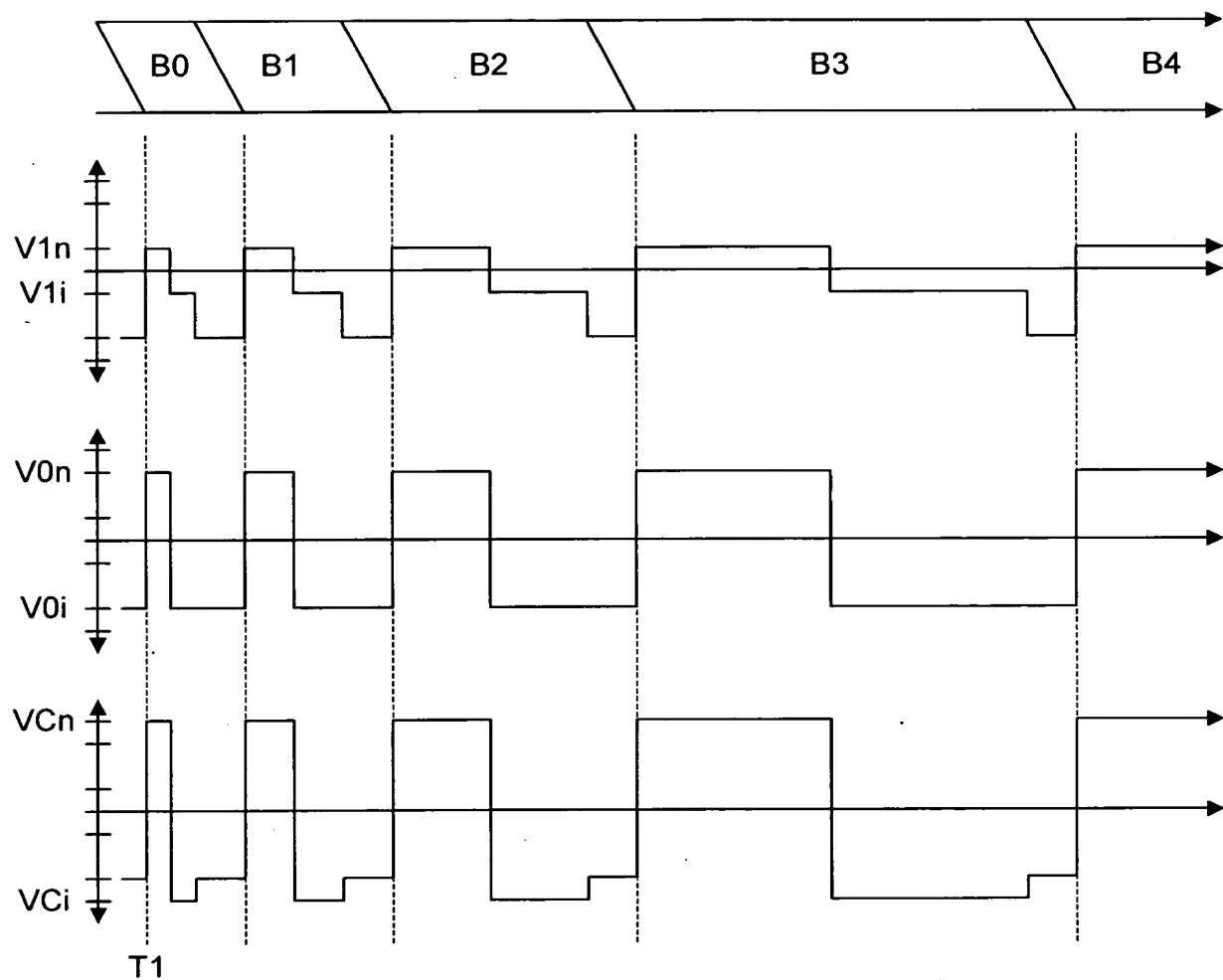


FIG. 25

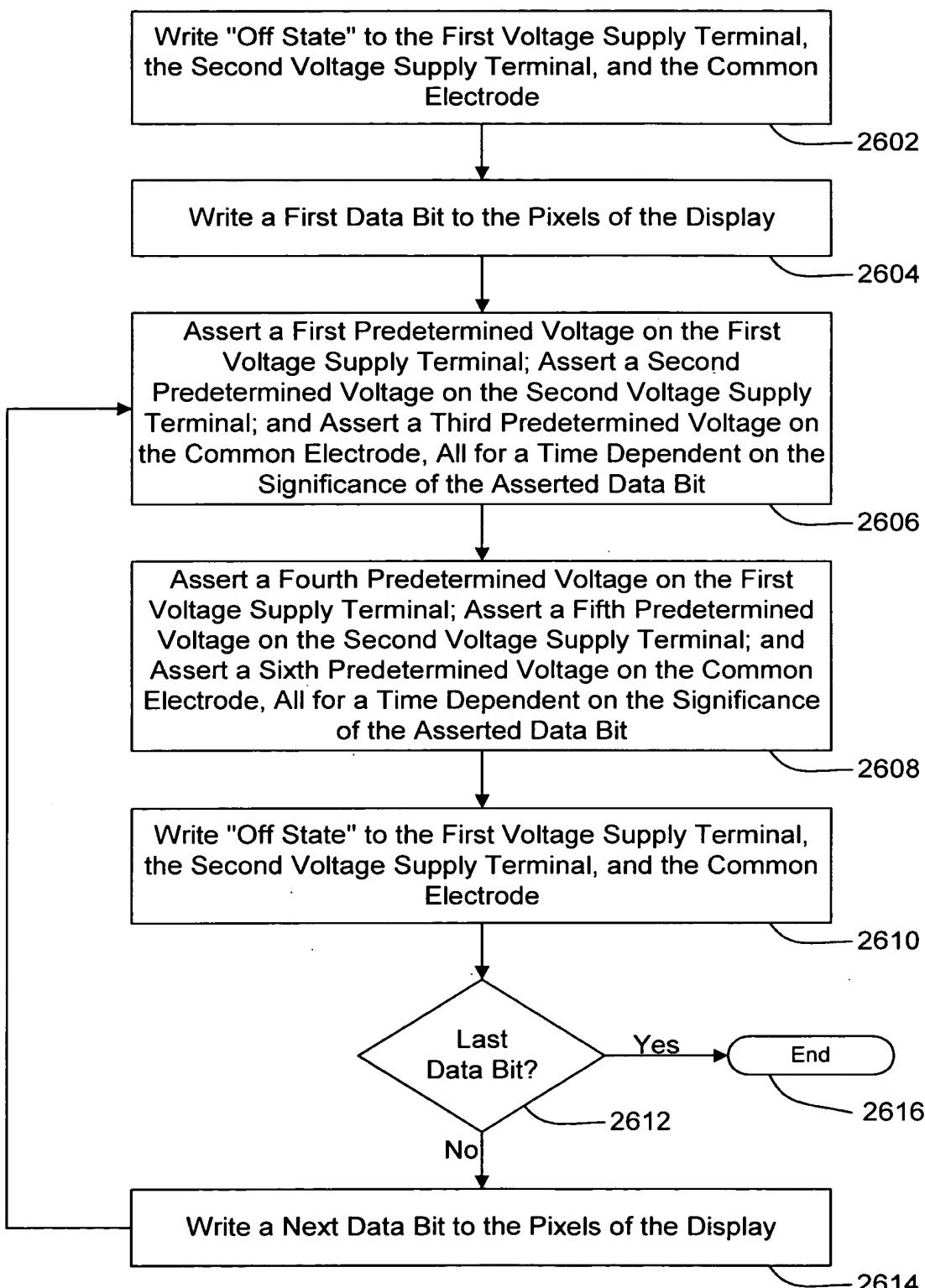


FIG. 26

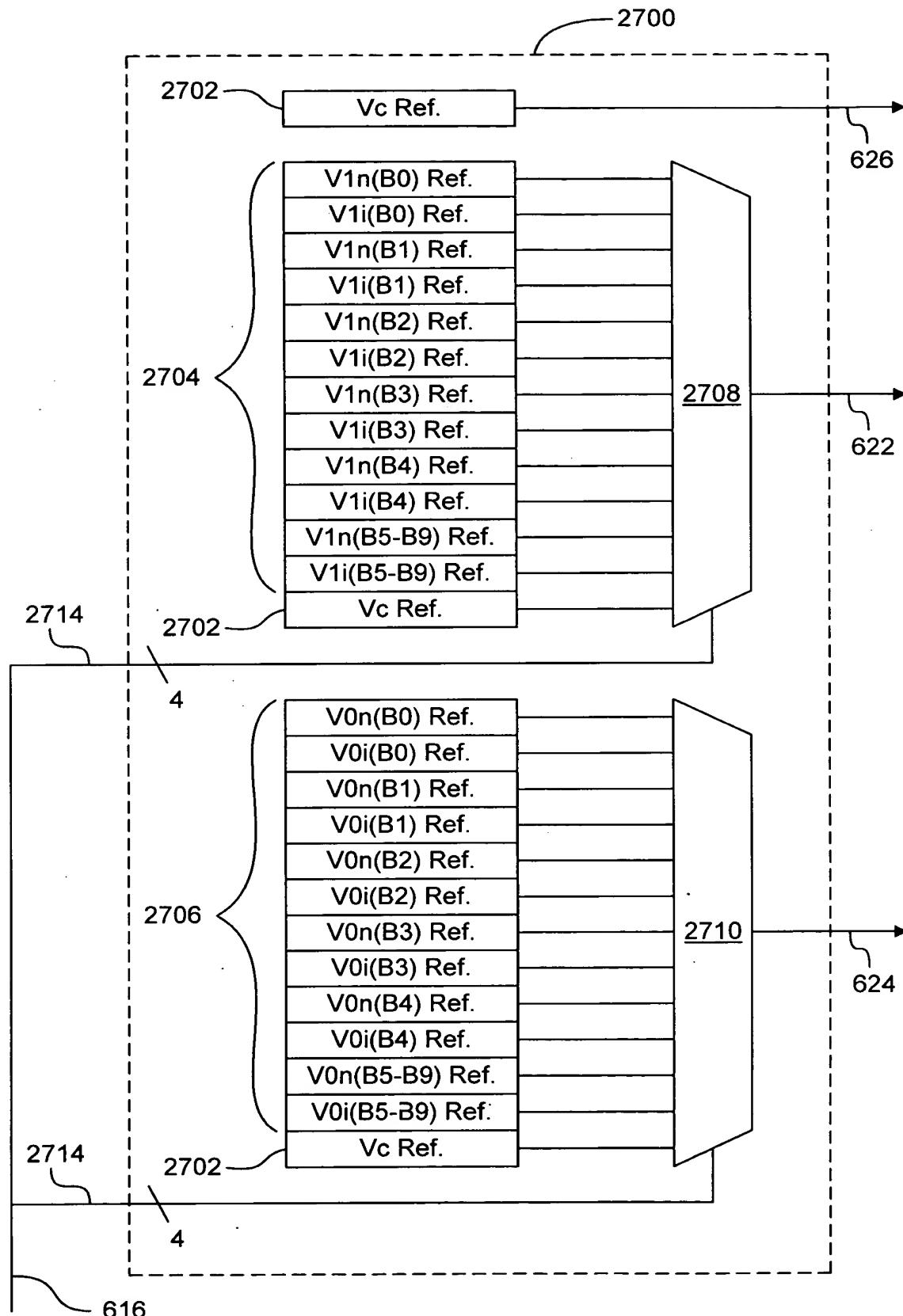


FIG. 27

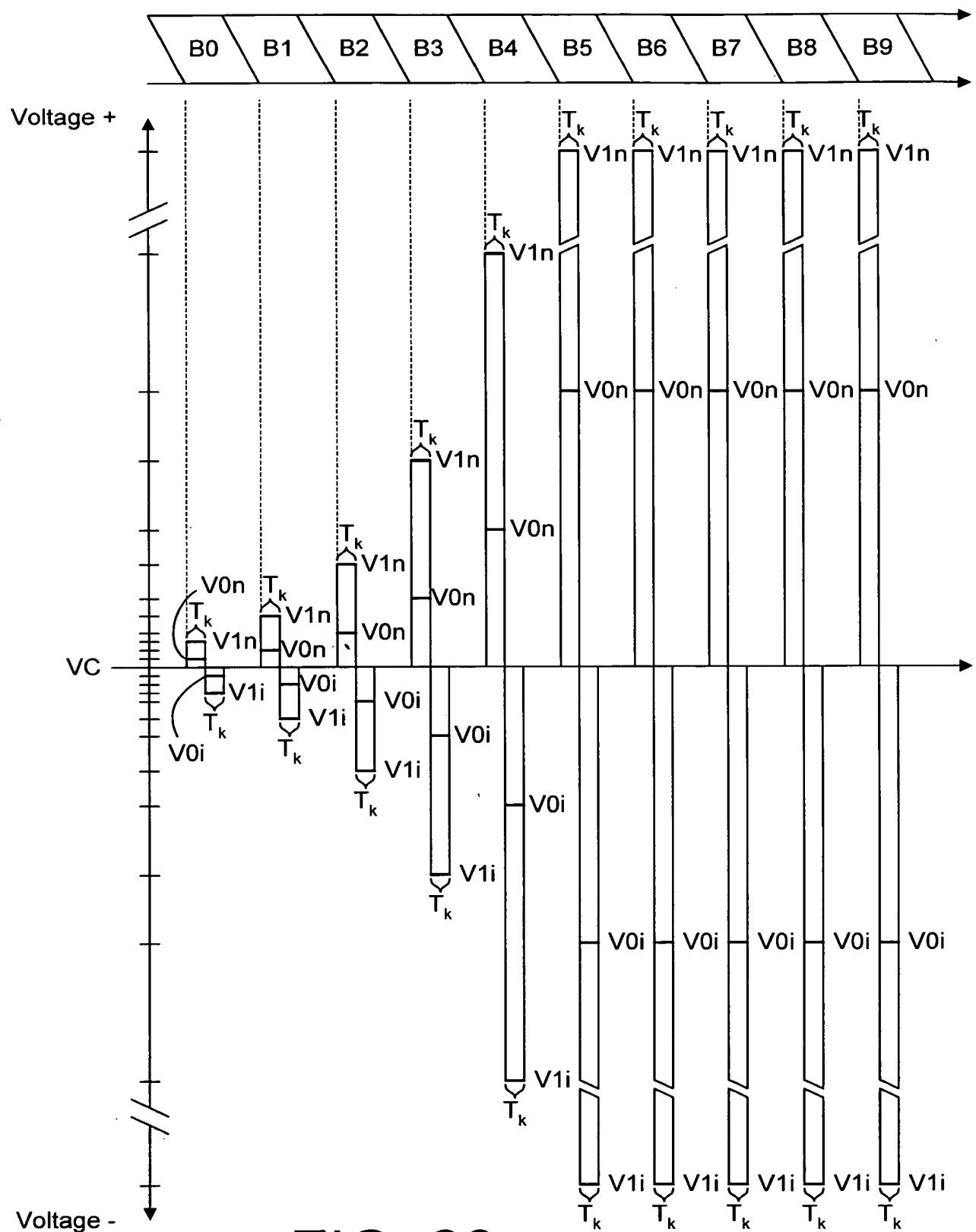


FIG. 28

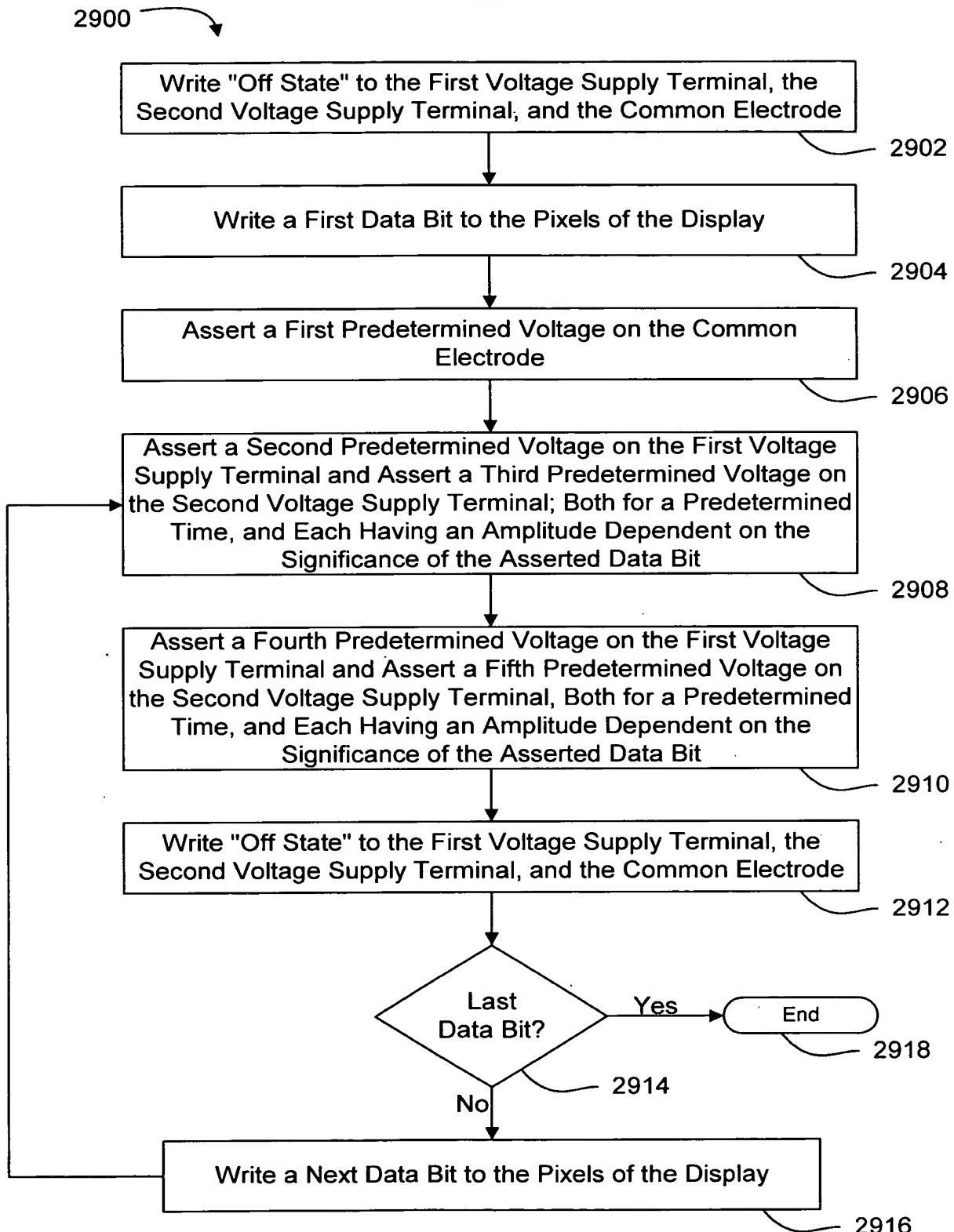


FIG. 29

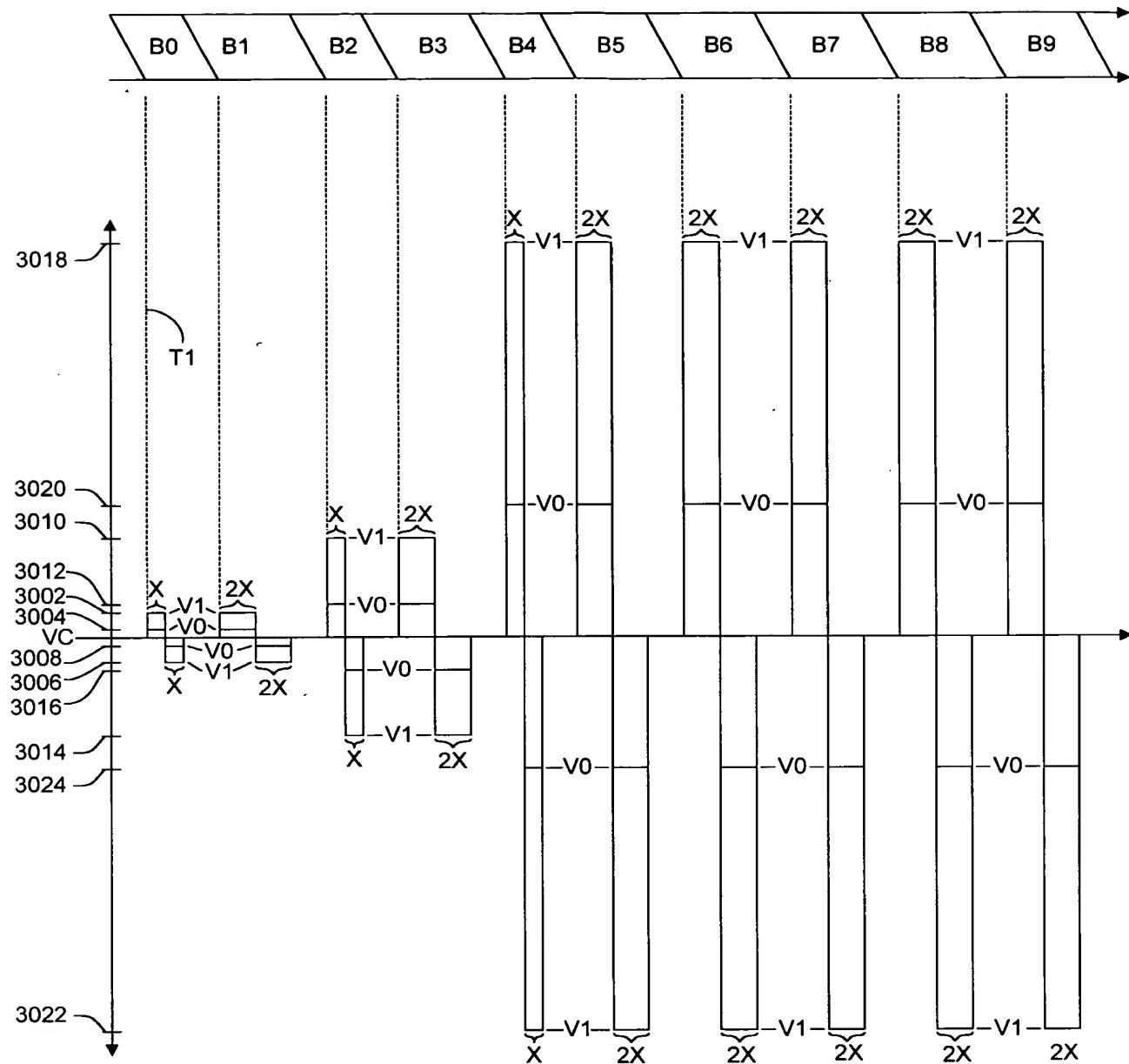


FIG. 30

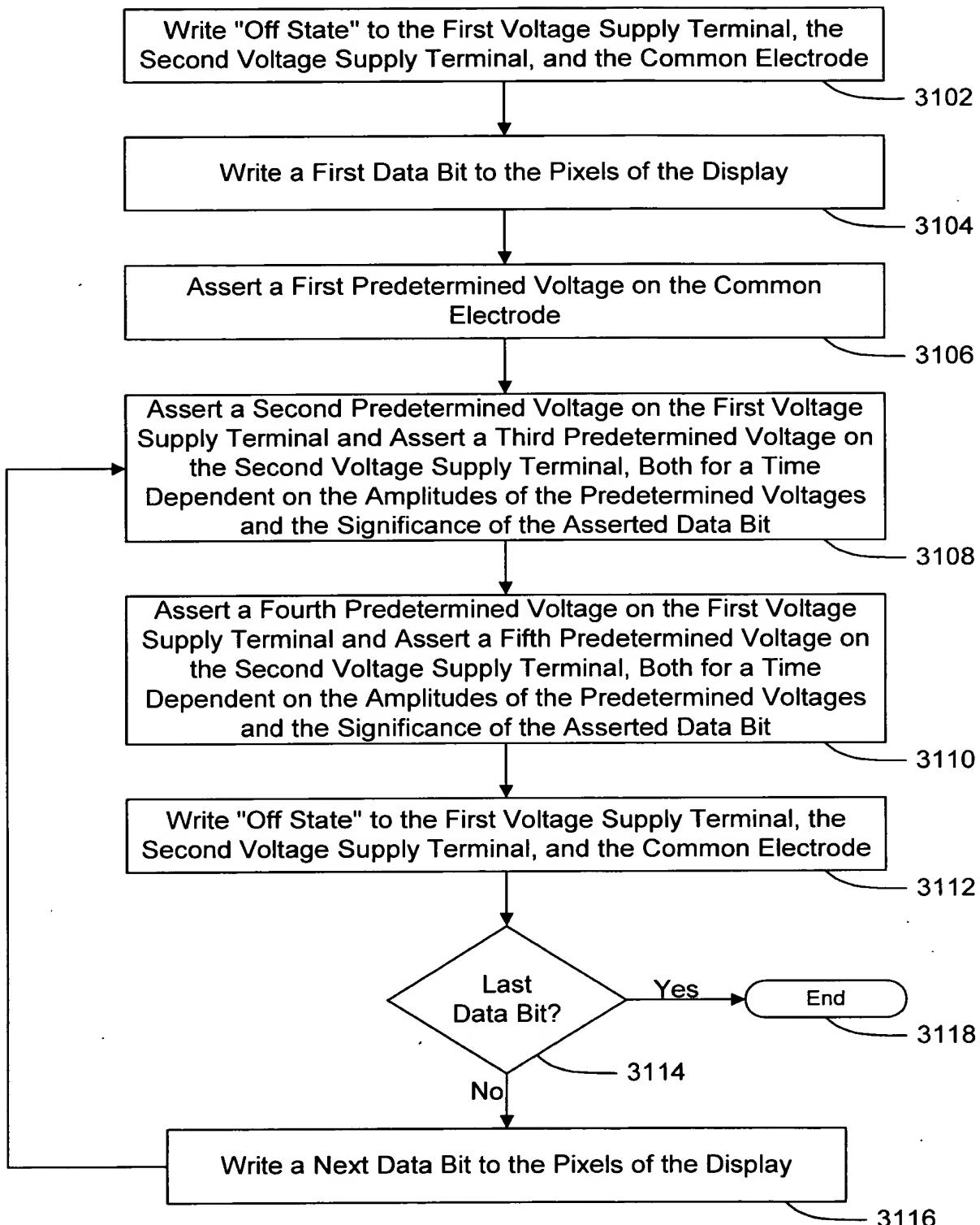


FIG. 31

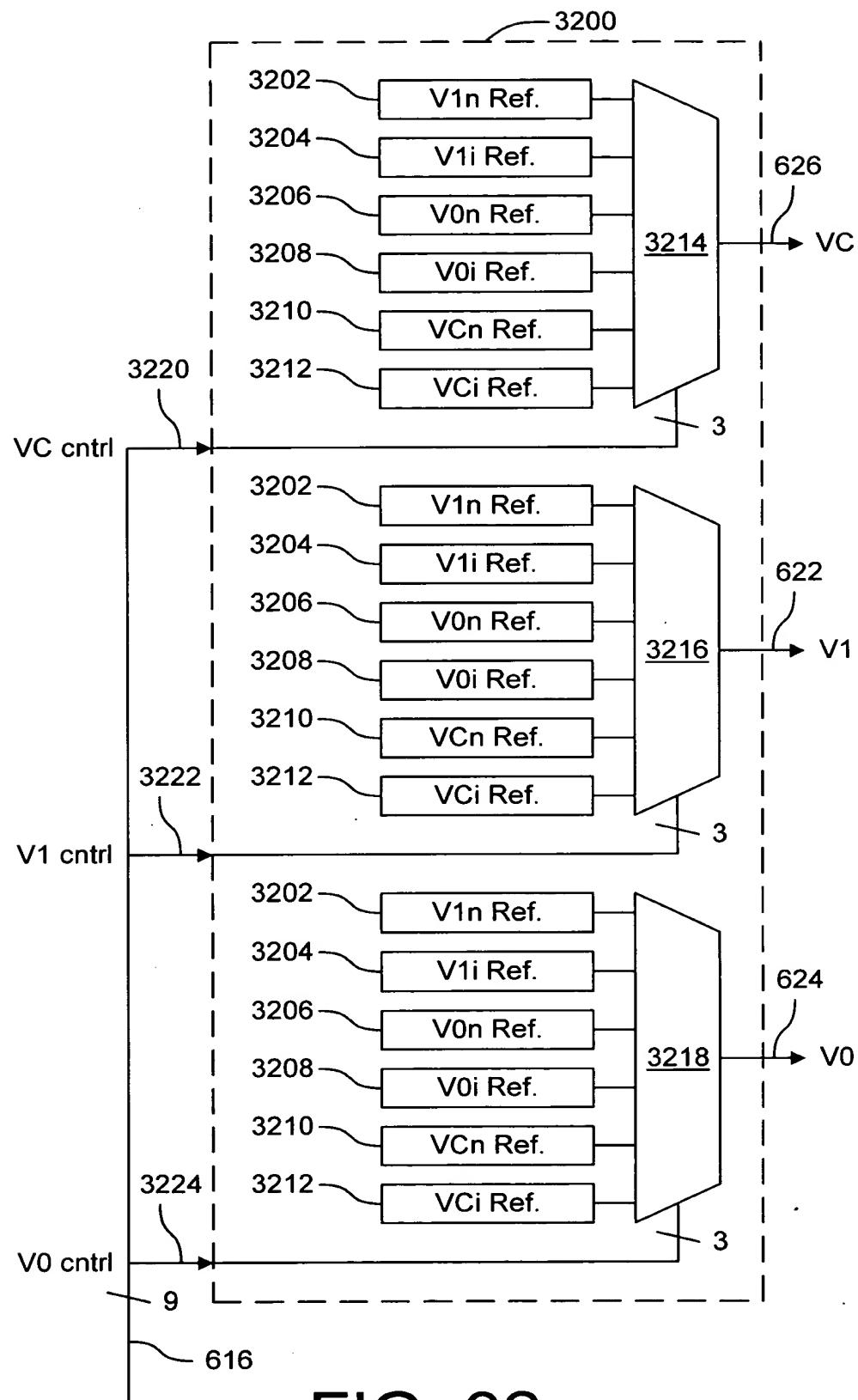


FIG. 32

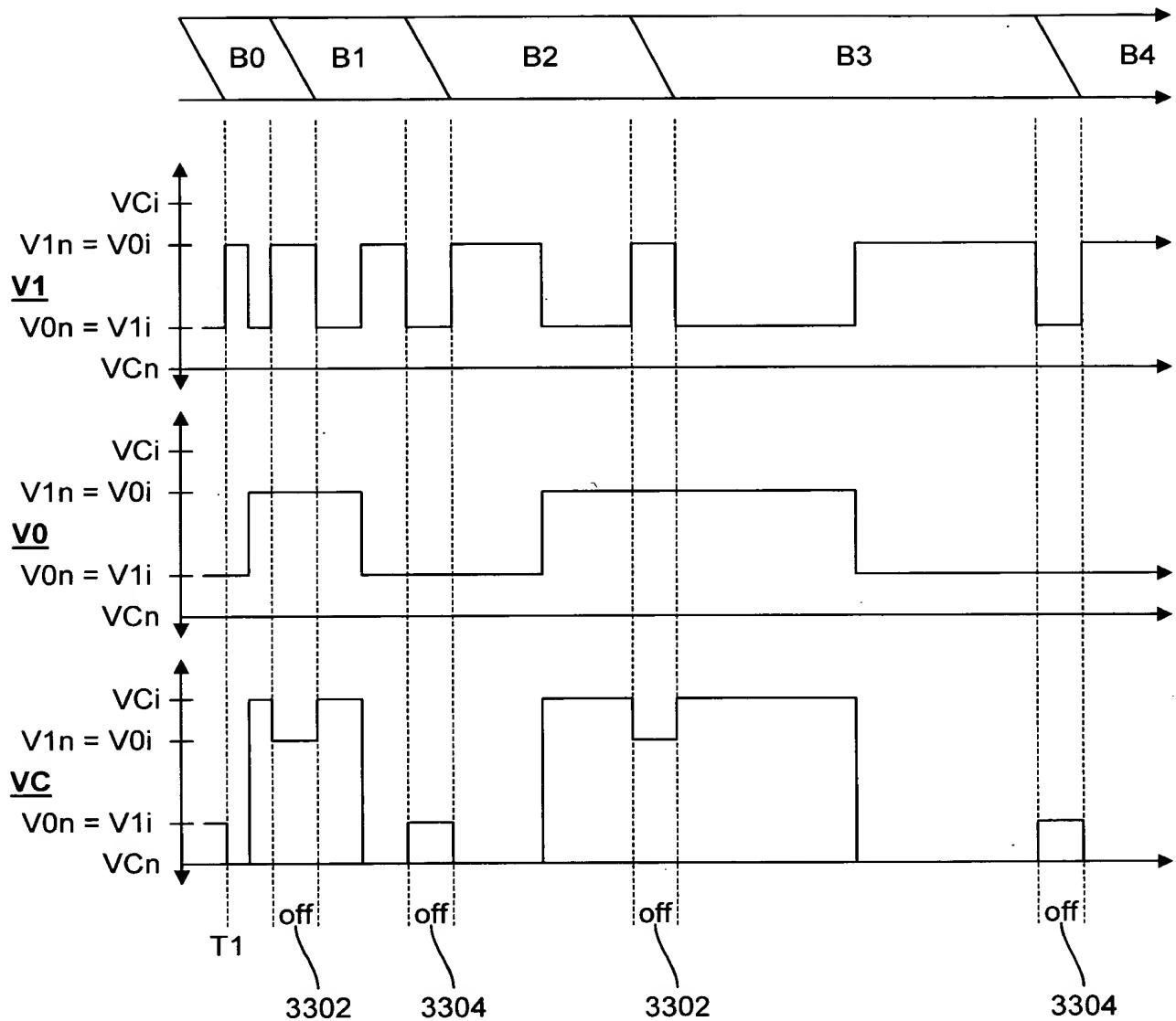
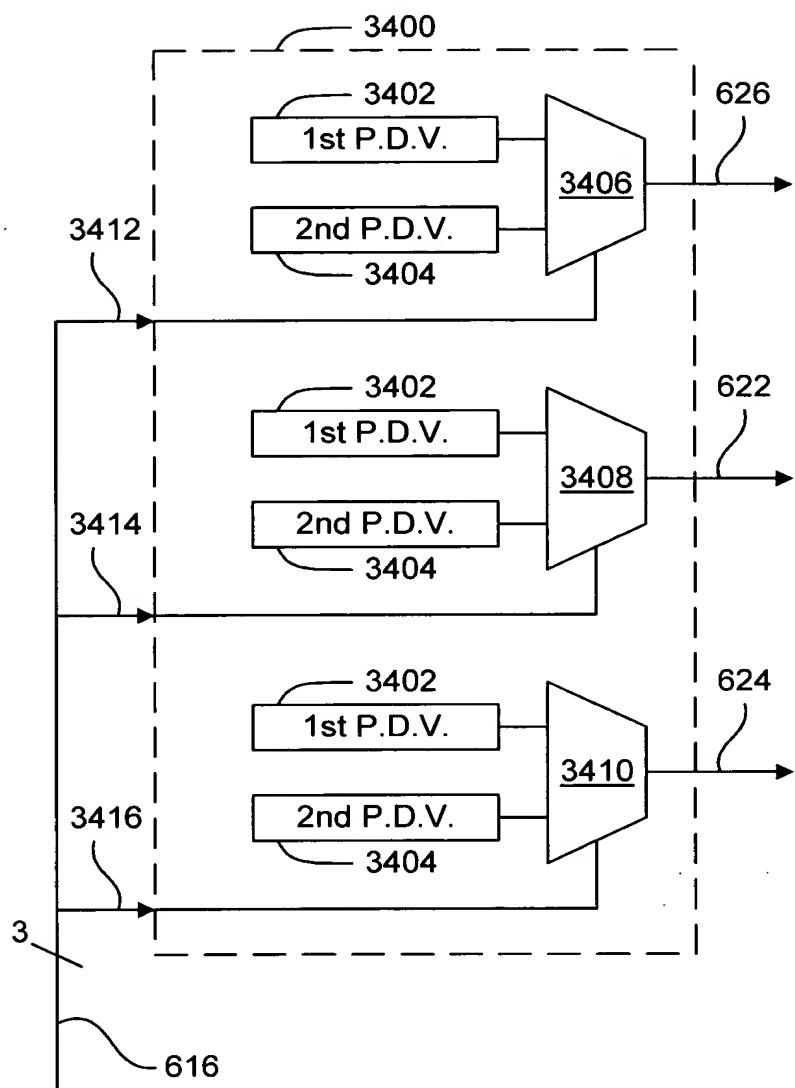


FIG. 33

**FIG. 34**

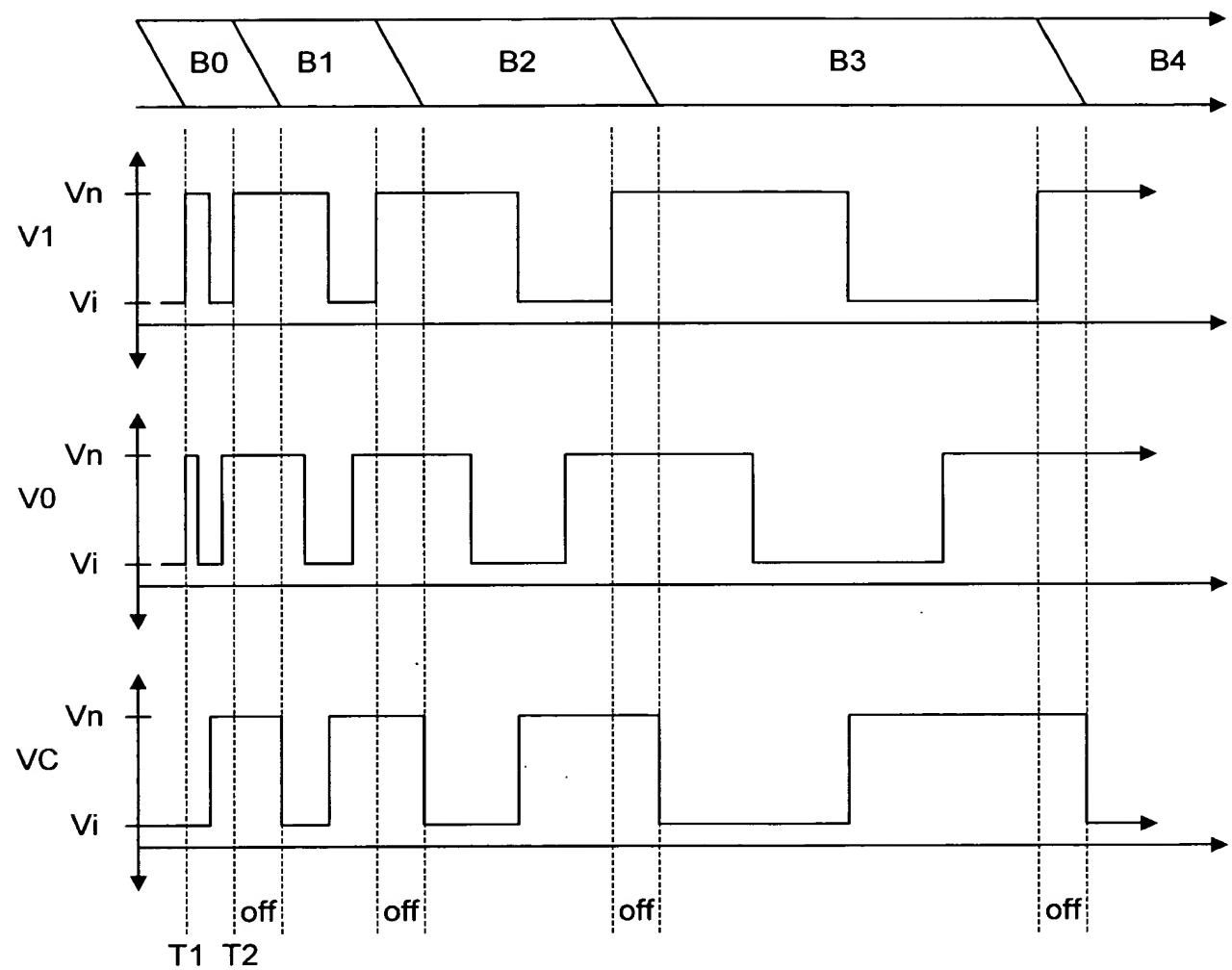


FIG. 35

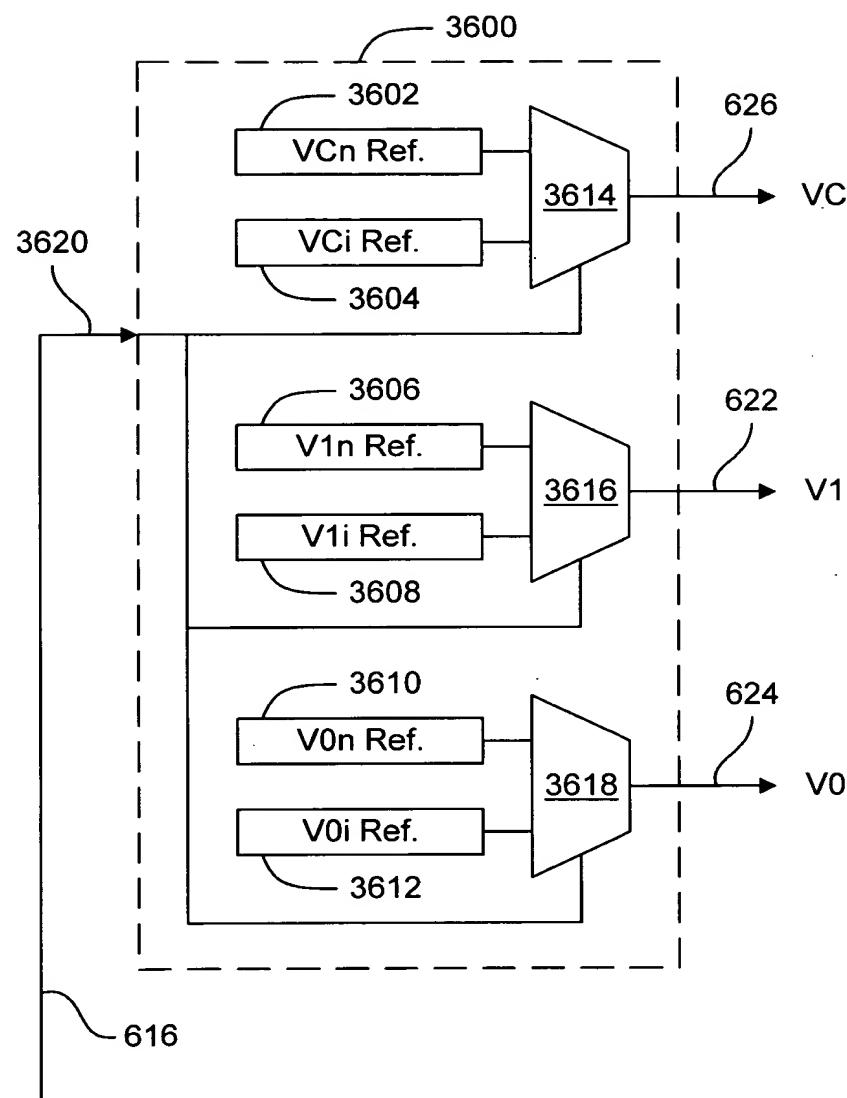


FIG. 36